

L Number	Hits	Search Text	DB	Time stamp
4	42	memory AND input AND output AND ((overheat (over ADJ heat)) NEAR (prevent prevention)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:06
3	11	memory AND input AND output AND (protect protection prevent prevention) AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:03
5	3	memory AND input AND output AND (((over ADJ current) overcurrent) NEAR (protect protection prevent prevention)) AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:13
6	29	memory AND input AND output AND (device appliance) AND ((overheat (over ADJ heat)) NEAR (prevent prevention)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:07
8	19	memory AND input AND output AND (device appliance) AND ((overheat (over ADJ heat)) NEAR (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:10
10	23	memory AND input AND output AND ((overheat (over ADJ heat)) ADJ (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:12
9	1	memory AND input AND output AND appliance AND ((overheat (over ADJ heat)) ADJ (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:11
11	21	memory AND input AND output AND off AND ((overheat (over ADJ heat)) ADJ (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:14
12	3	memory AND input AND output AND off AND (((over ADJ current) overcurrent) NEAR (protect protection prevent prevention)) AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:13
13	6	memory AND input AND output AND (off NEAR (motor drive relay)) AND ((overheat (over ADJ heat)) ADJ (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:15

14	9	memory AND input AND output AND (off WITH (motor drive relay)) AND ((overheat (over ADJ heat)) ADJ (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:17
15	10	memory AND input AND output AND (off WITH (motor drive load)) AND ((overheat (over ADJ heat)) ADJ (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:19
-	296	700/11 AND memory AND input AND output AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:37
-	112	700/11 AND memory AND input AND output AND interface AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 13:01
-	42	700/11 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 13:11
-	34	700/11 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND (load condition) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 13:09
-	15	700/11 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND load AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 13:19
-	42	700/11 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND (device load appliance) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:05
-	4	700/11 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:40

-	89	700/17 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND (appliance load) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:44
-	25	700/17 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:53
-	87	700/17 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND (appliance load) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:55
-	1	700/23 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (feedback display visual audible interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:55
-	13	700/23 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND (appliance load) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 14:57
-	9	700/20 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND (appliance load) AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:05
-	15	700/20 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND device AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:07
-	20	700/23 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND device AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:23
-	125	700/17 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND device AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:35

-	25	700/17 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:37
-	1	700/23 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:26
-	2	700/20 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:26
-	24	700/17 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (logic software) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 15:39
-	2	700/11 AND memory AND (process processing processor control controlling controller) AND (logic software) AND (actuator sensor actuating sensing) AND (display interface panel terminal) AND appliance AND ((voltage power) NEAR supply) AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:08
-	2	700/11 AND memory AND (process processing processor control controlling controller) AND (configure configuring configuration) AND (logic software) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND appliance AND power AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:08
-	2	700/20 AND memory AND (process processing processor control controlling controller) AND (logic software) AND (actuator sensor actuating sensing) AND (display interface panel terminal) AND appliance AND ((voltage power) NEAR supply) AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:09
-	2	700/23 AND memory AND (process processing processor control controlling controller) AND (logic software) AND (actuator sensor actuating sensing) AND (display interface panel terminal) AND appliance AND ((voltage power) NEAR supply) AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:17
-	18	700/17 AND memory AND (process processing processor control controlling controller) AND (logic software) AND (actuator sensor actuating sensing) AND (display interface panel terminal) AND appliance AND ((voltage power) NEAR supply) AND (clock timer counter) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:14

-	0	700/23 AND interface NEAR (input AND (user operator) AND (appliance device load) AND (power supply) AND (network communicaiton)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:19
-	0	700/\$ AND interface NEAR (input AND (user operator) AND (appliance device load) AND (power supply) AND (network communicaiton)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:20
-	119	700/\$ AND interface WITH (input AND (user operator) AND (appliance device load) AND (power supply) AND (network communicaiton)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:19
-	38	700/\$ AND interface WITH (input AND (user operator) AND (appliance load) AND (power supply) AND (network communicaiton)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/03/26 17:29
-	126	overcurrent and protection and soft ADJ start AND load AND current AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 14:46
-	70	overcurrent WITH protection AND soft ADJ start AND load AND current AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 14:47
-	5	overcurrent ADJ protection AND soft ADJ start AND load AND rush ADJ current AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 14:47
-	5	overcurrent WITH protection AND soft ADJ start AND load AND rush ADJ current AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:04
-	0	700/\$ AND (overcurrent protection) AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:11
-	75	700/\$ AND overheat AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:16
-	3441	700/\$ AND (overcurrent ADJ protection) (soft ADJ start) (rush ADJ current) AND load AND input AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:19

-	3	700/\$ AND overheat ADJ protection AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:14
-	1	700/\$ AND ((overcurrent ADJ protection) (soft ADJ start)) AND (rush ADJ current) AND load AND input AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:17
-	13	700/\$ AND interface WITH (input AND (user operator) AND (appliance device load)) AND ((overcurrent ADJ protection) (soft ADJ start) (rush ADJ current)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:21
-	65	700/\$ AND (input AND (user operator) AND (appliance device load)) AND ((overcurrent ADJ protection) (soft ADJ start) (rush ADJ current)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:22
-	48	700/\$ AND (input AND (user operator) AND (appliance device load)) AND overheat AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:22
-	3	(700/\$ AND (input AND (user operator) AND (appliance device load)) AND ((overcurrent ADJ protection) (soft ADJ start) (rush ADJ current)) AND (@ad<20000107 @rlad<20000107)) AND (700/\$ AND (input AND (user operator) AND (appliance device load)) AND overheat AND (@ad<20000107 @rlad<20000107))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:23
-	2	700/\$ AND memory AND (process processing processor control controlling controller) AND (actuator sensor signal sensing) AND (display interface panel terminal) AND (overheat over ADJ heat) AND (over ADJ current overcurrent) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:39
-	2	700/\$ AND memory AND input AND output AND (overheat over ADJ heat) AND (over ADJ current overcurrent) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 15:59
-	0	700/\$ AND memory AND input AND output AND ((over ADJ current) overcurrent) AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:01
-	60	700/\$ AND memory AND input AND output AND (overheat (over ADJ heat)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:05

-	237	700/\$ AND memory AND input AND output AND ((over ADJ current) overcurrent) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:04
-	1	700/\$ AND memory AND input AND output AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:18
-	3	700/\$ AND memory AND input AND output AND ((overheat (over ADJ heat)) ADJ protection) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:09
-	30	700/\$ AND memory AND input AND output AND ((overcurrent (over ADJ current)) ADJ protection) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:11
-	3	700/\$ AND memory AND input AND output AND ((overheat (over ADJ heat)) NEAR (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:17
-	5	700/\$ AND memory AND input AND output AND ((overheat (over ADJ heat)) WITH (protect protection)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/04 16:18
-	2	700/\$ AND memory AND input AND output AND ((overheat (over ADJ heat)) NEAR (prevent prevention)) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:01
-	1	700/\$ AND memory AND input AND output AND (protect protection prevent prevention) AND (soft ADJ start) AND (rush ADJ current) AND (@ad<20000107 @rlad<20000107)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/05 17:00